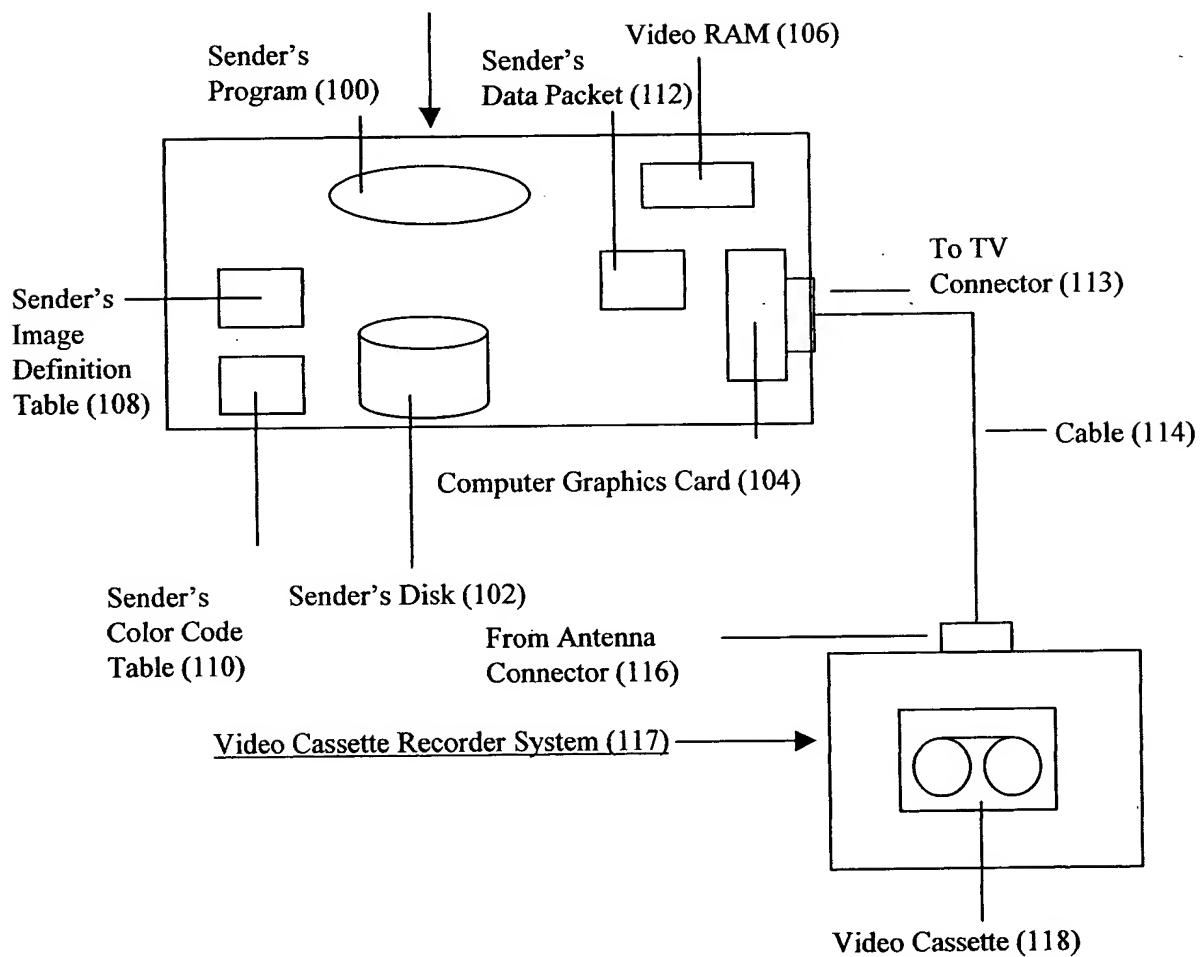


Sender's Computer System (101)**Fig. 1**

+

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Sender's Computer System (101)

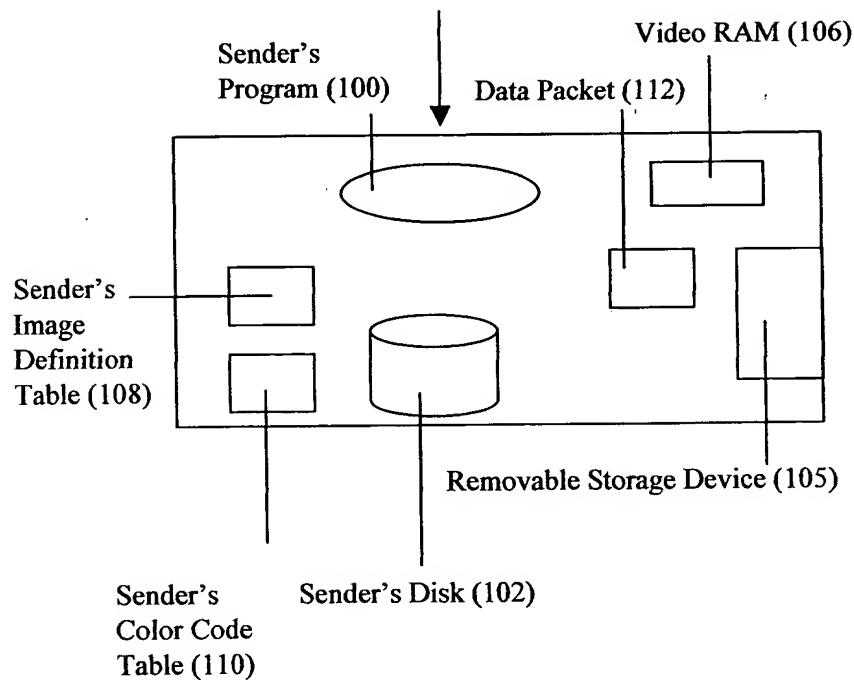


Fig. 1a

+

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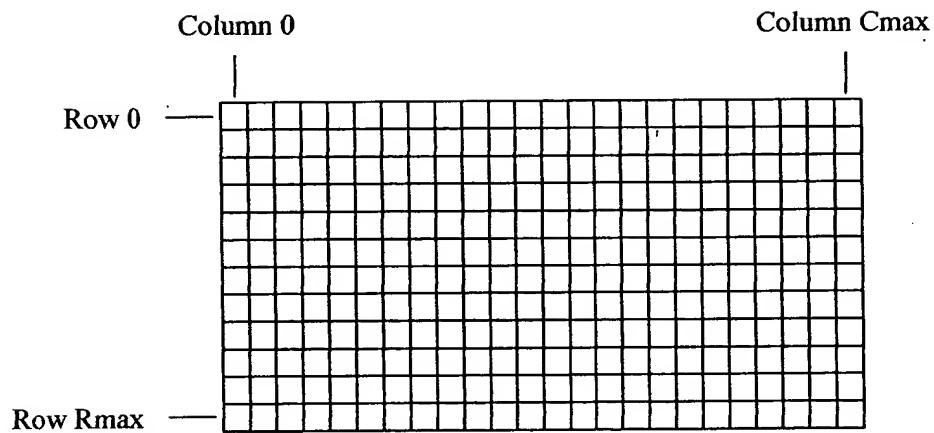


Fig. 2

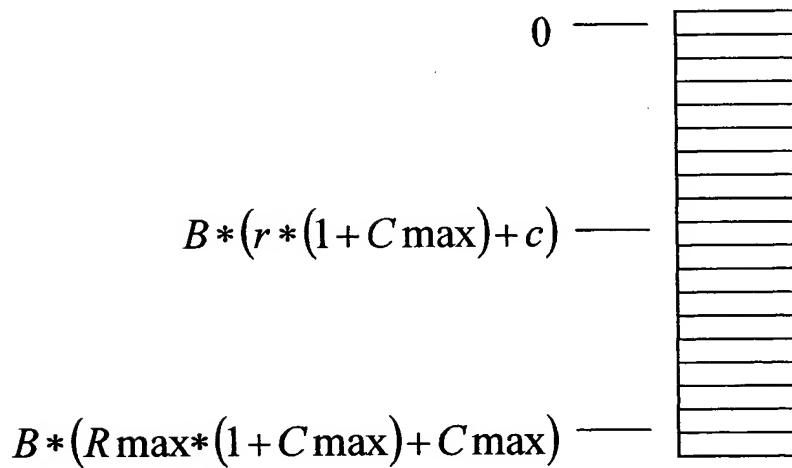


Fig. 3

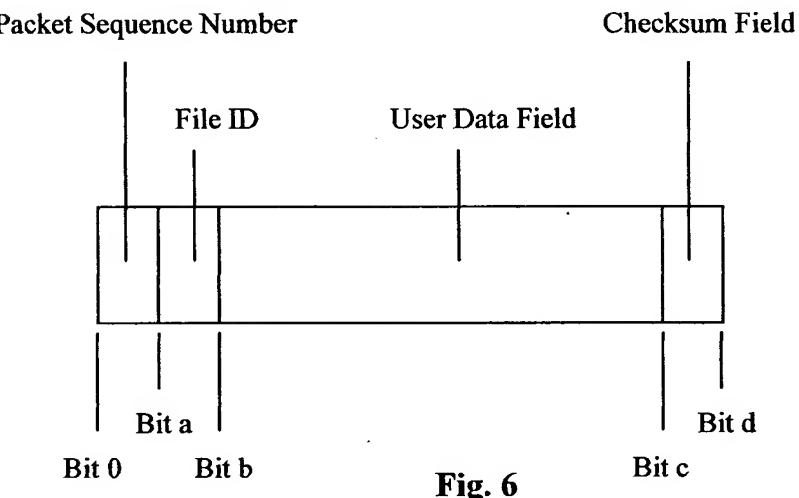
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<u>Image Area</u>	<u>Ordered Set of Memory Address Offsets</u>
0	{ Set 0}
1	{ Set 1}
2	{ Set 2}
Amax	{ Set Amax}

Fig. 4

Row	Red	Green	Blue	Color Value
0	r0	g0	b0	c0
1	r1	g1	b1	c1
2	r2	g2	b2	c2
..				
..				
..				
$2^N - 1$				

Fig. 5



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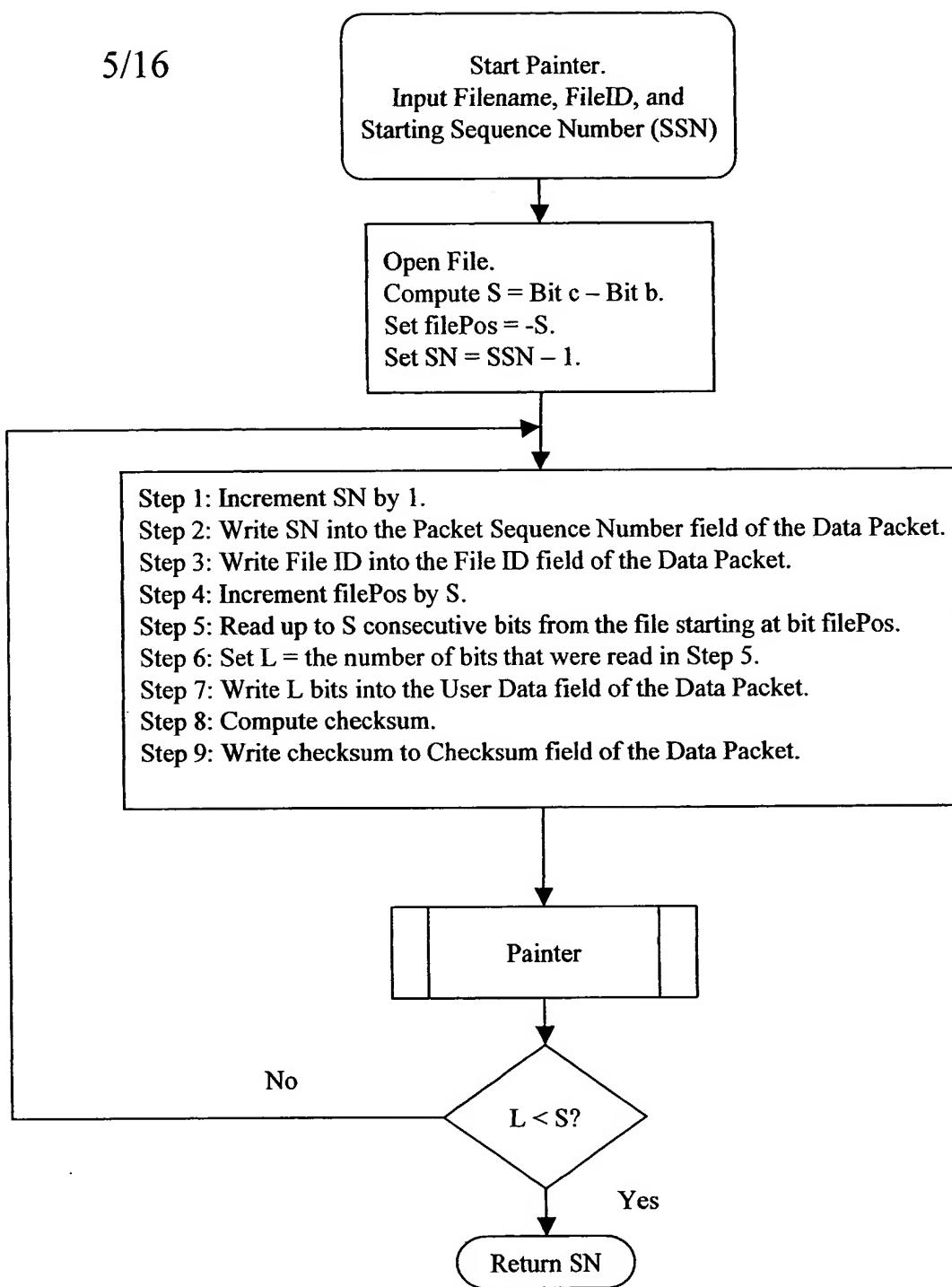


Fig. 7

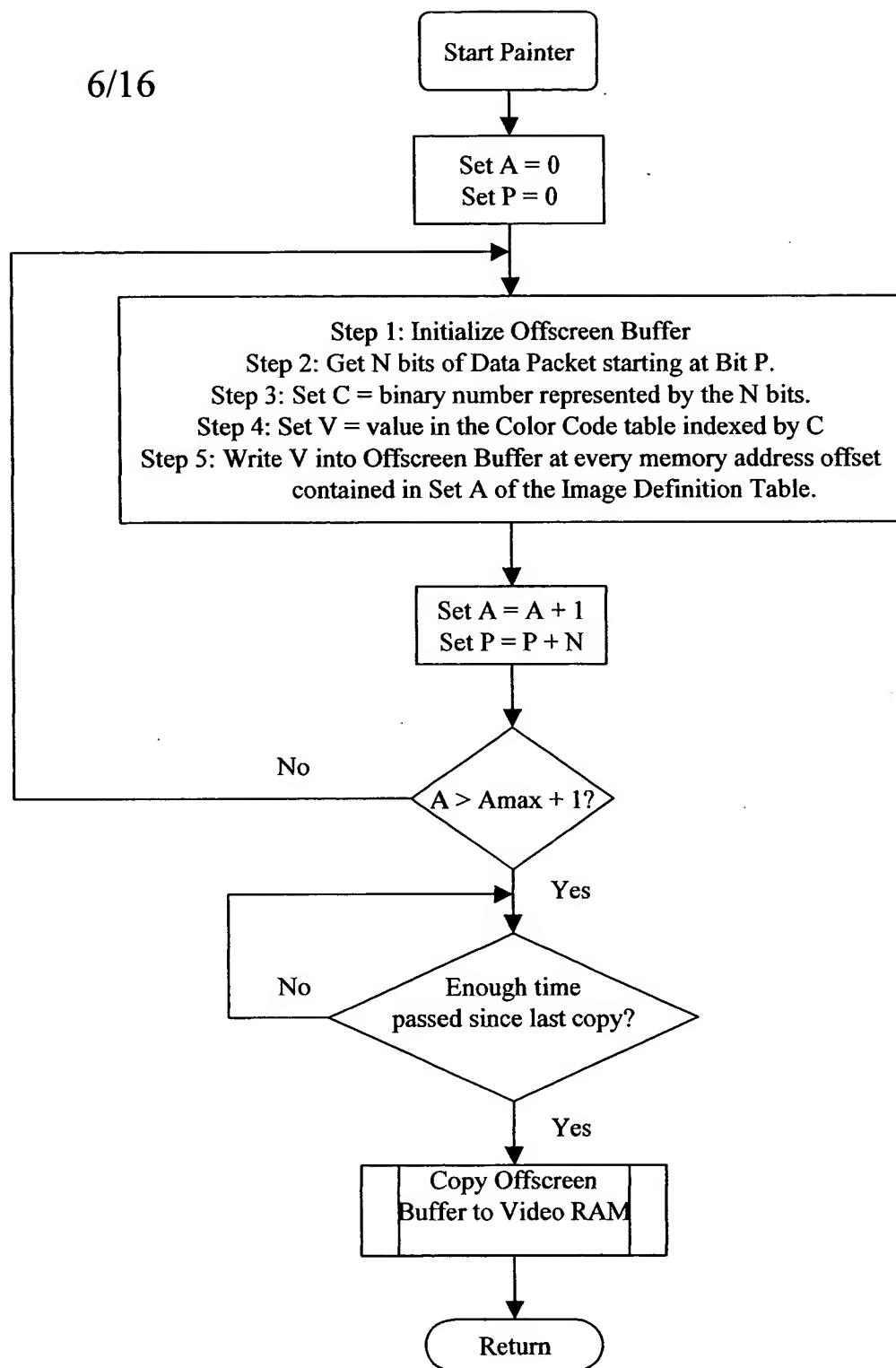


Fig. 8

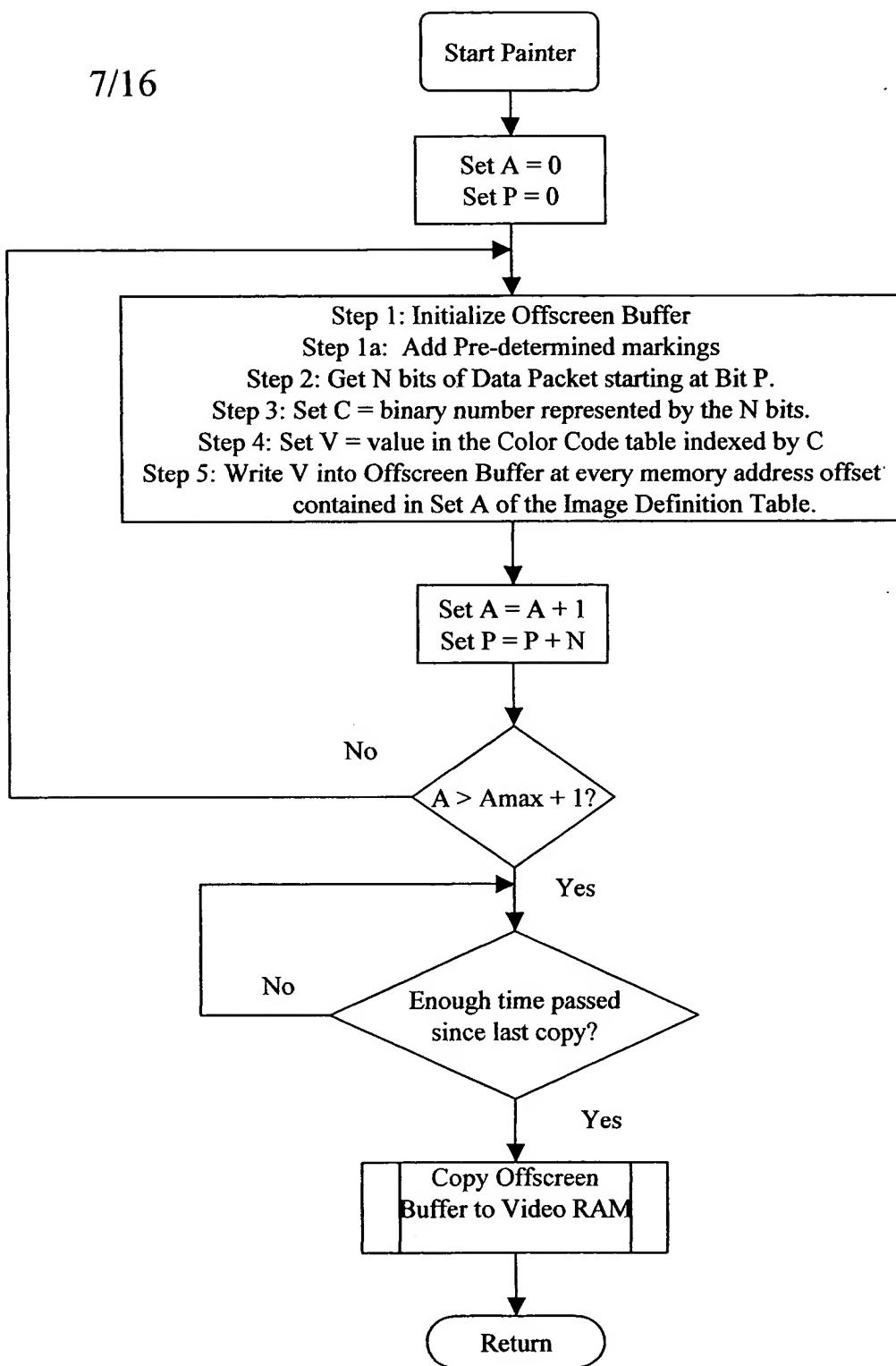


Fig. 8a

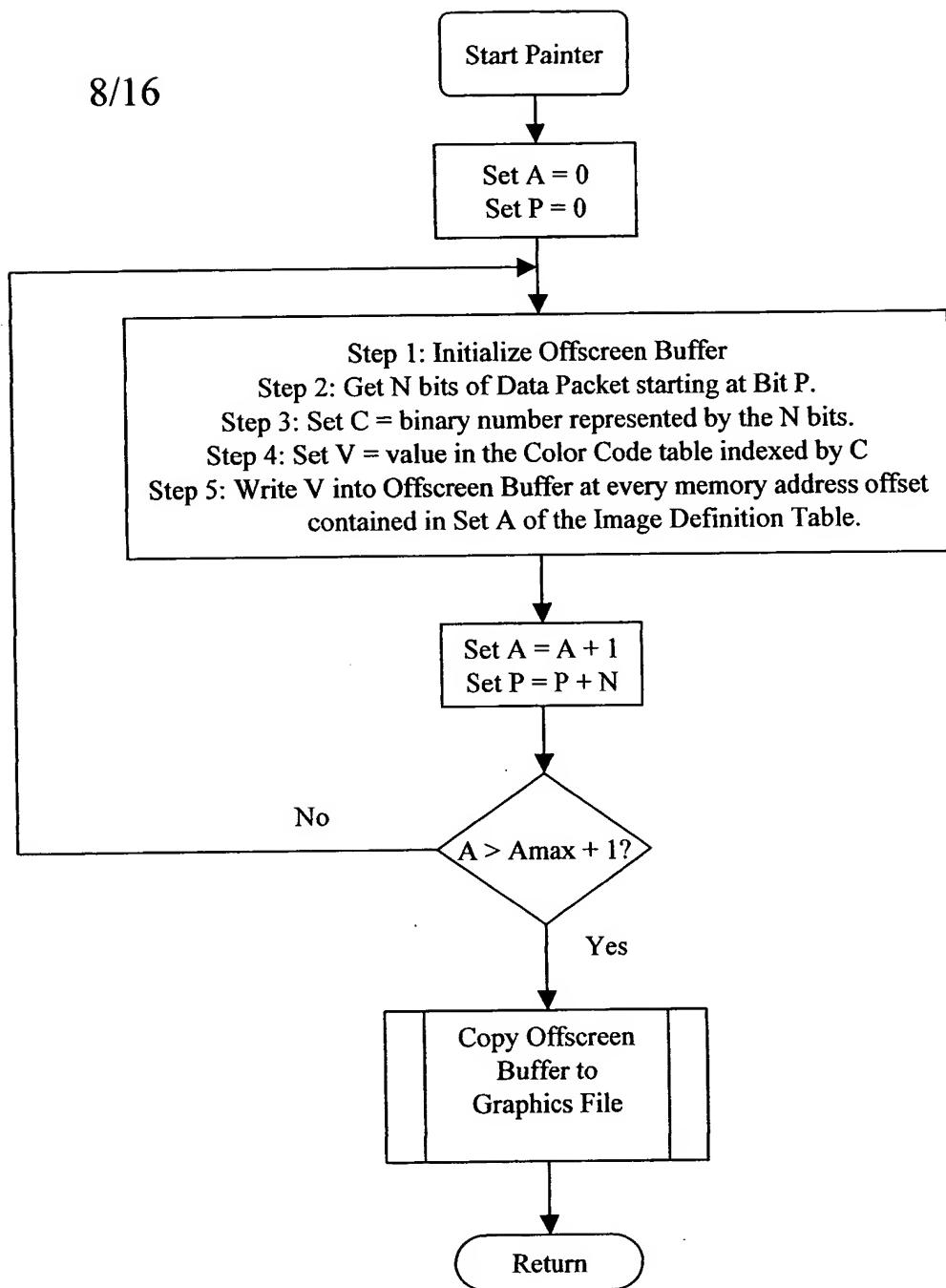


Fig. 8b

9/16 Receiver's Computer System (201)

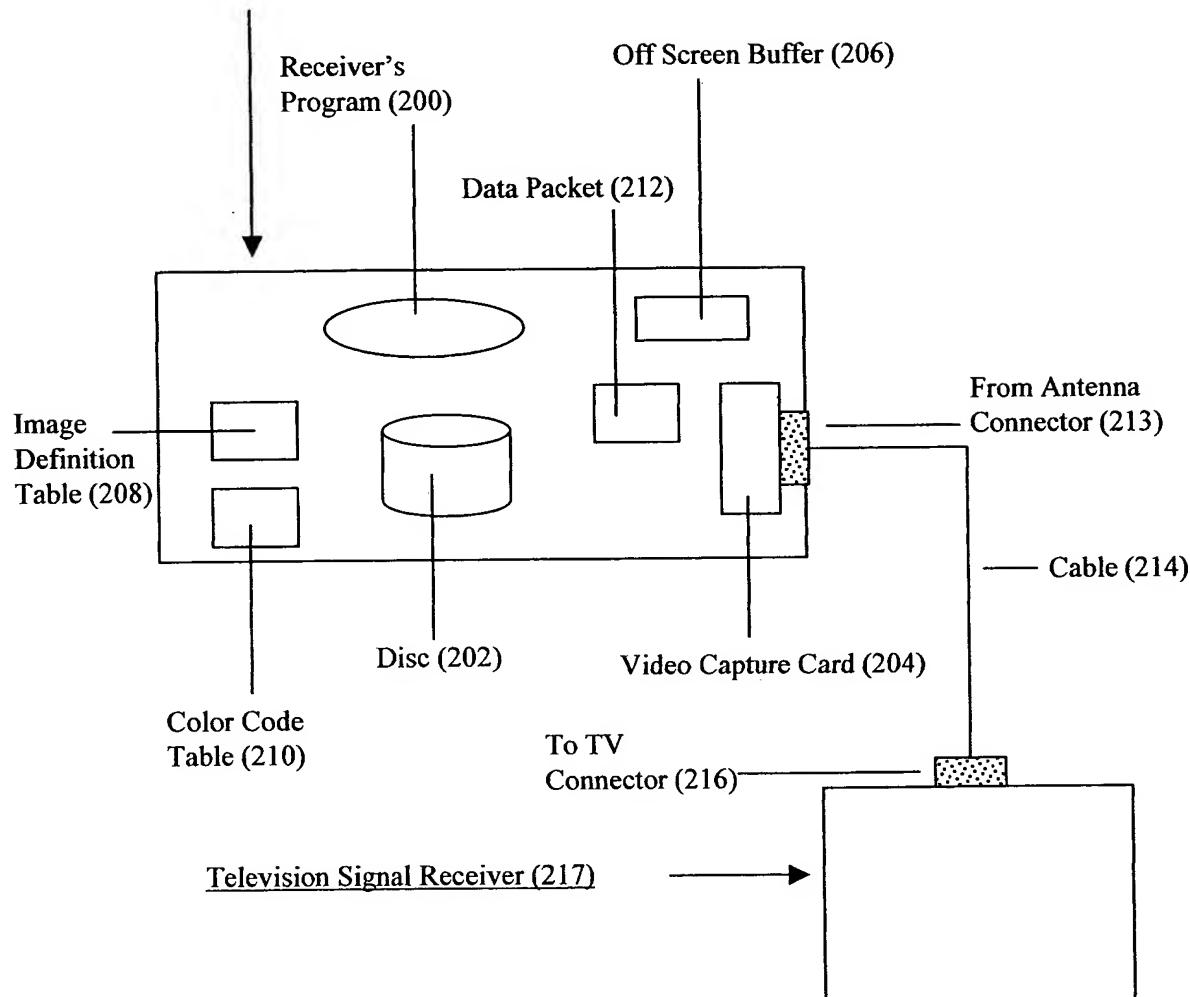


Fig. 9

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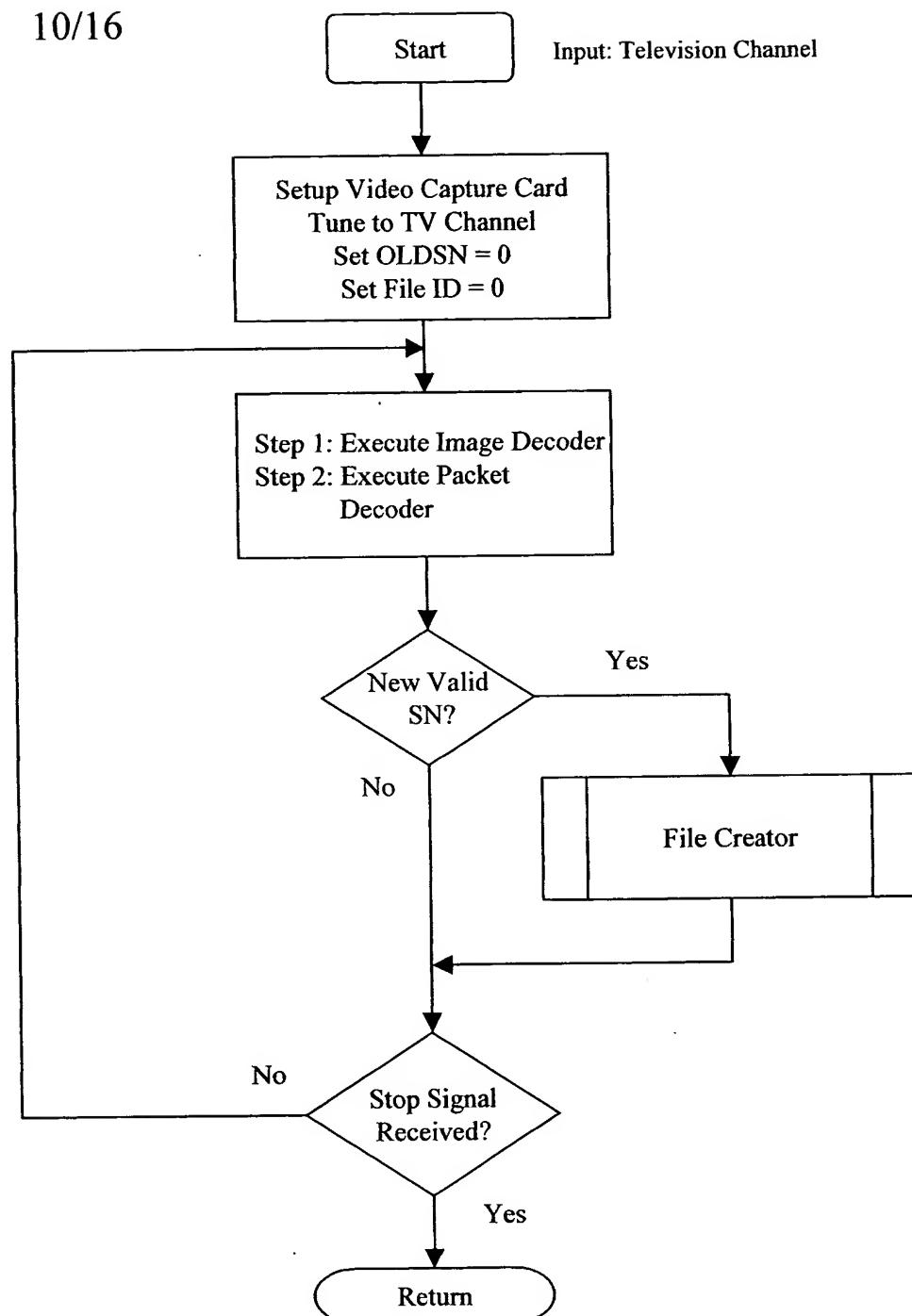


Fig. 10

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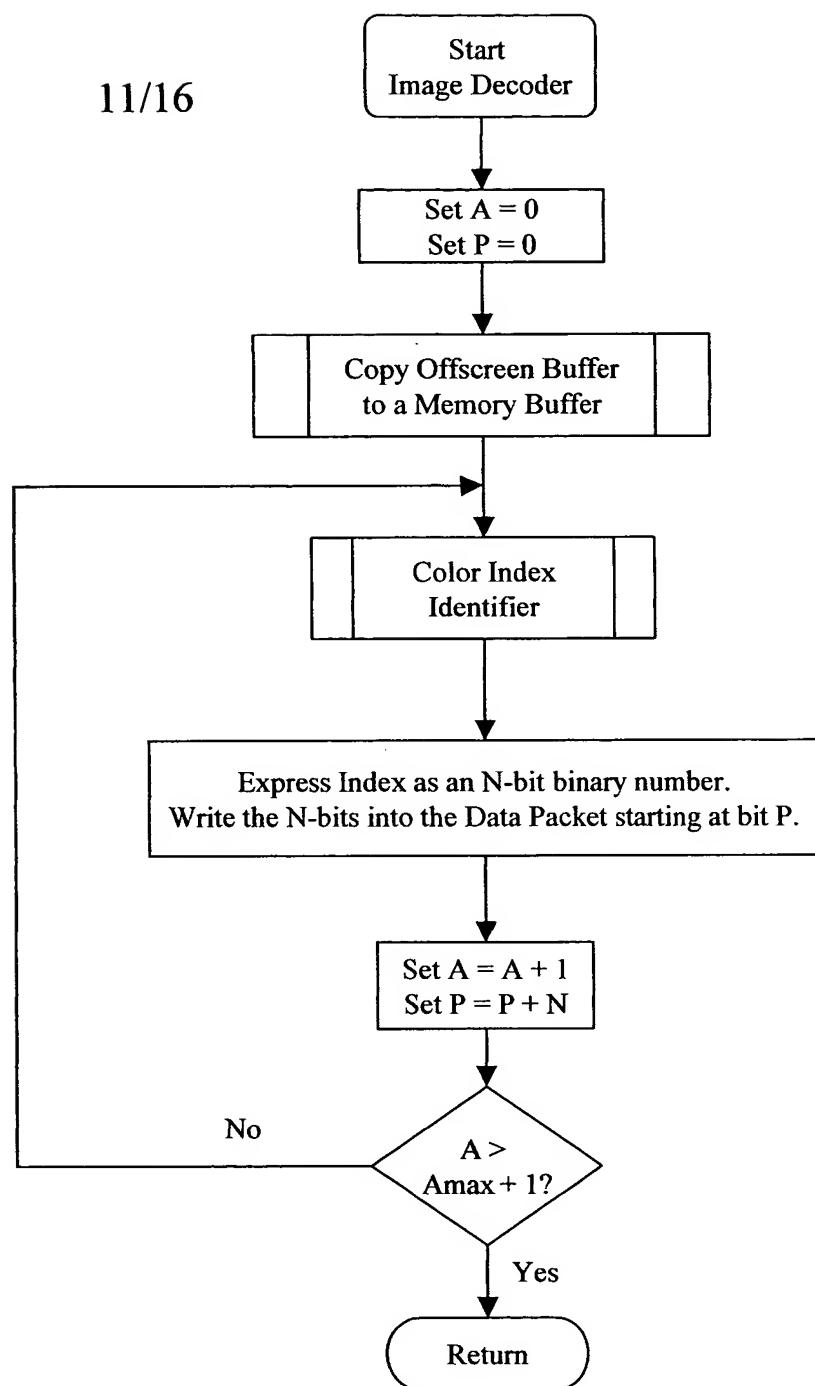
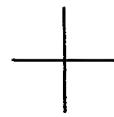


Fig. 11

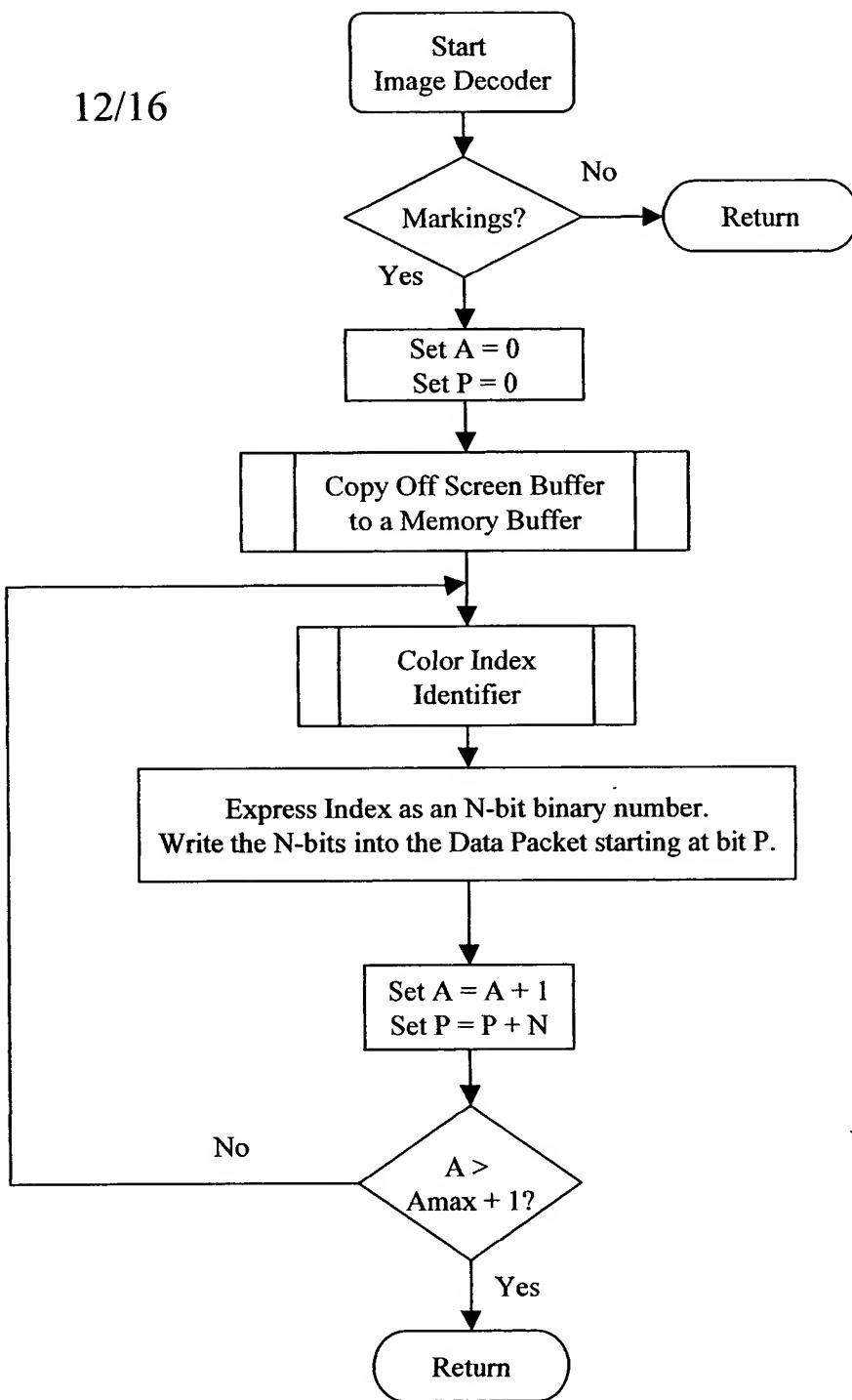
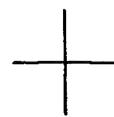


Fig. 11a

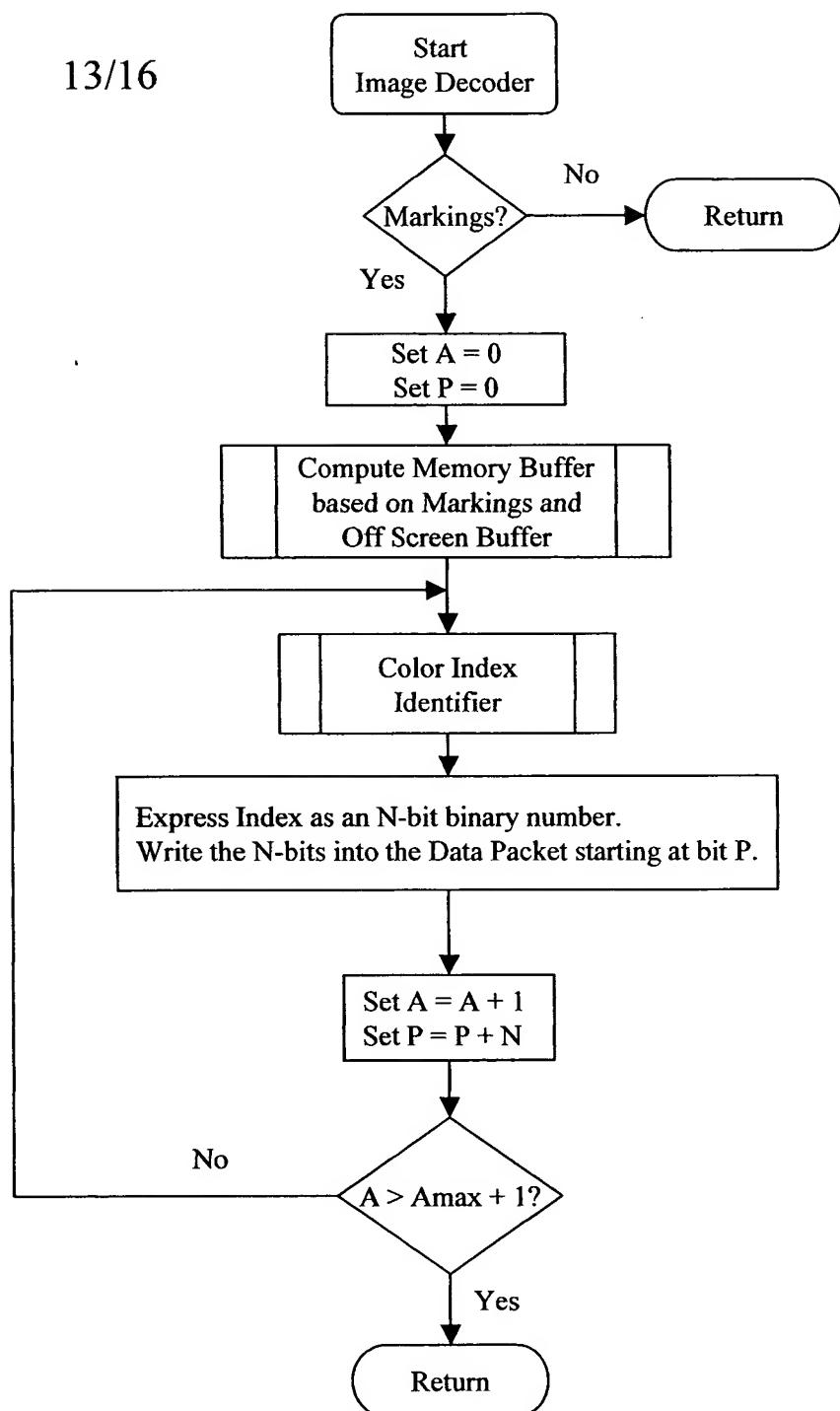


Fig. 11b

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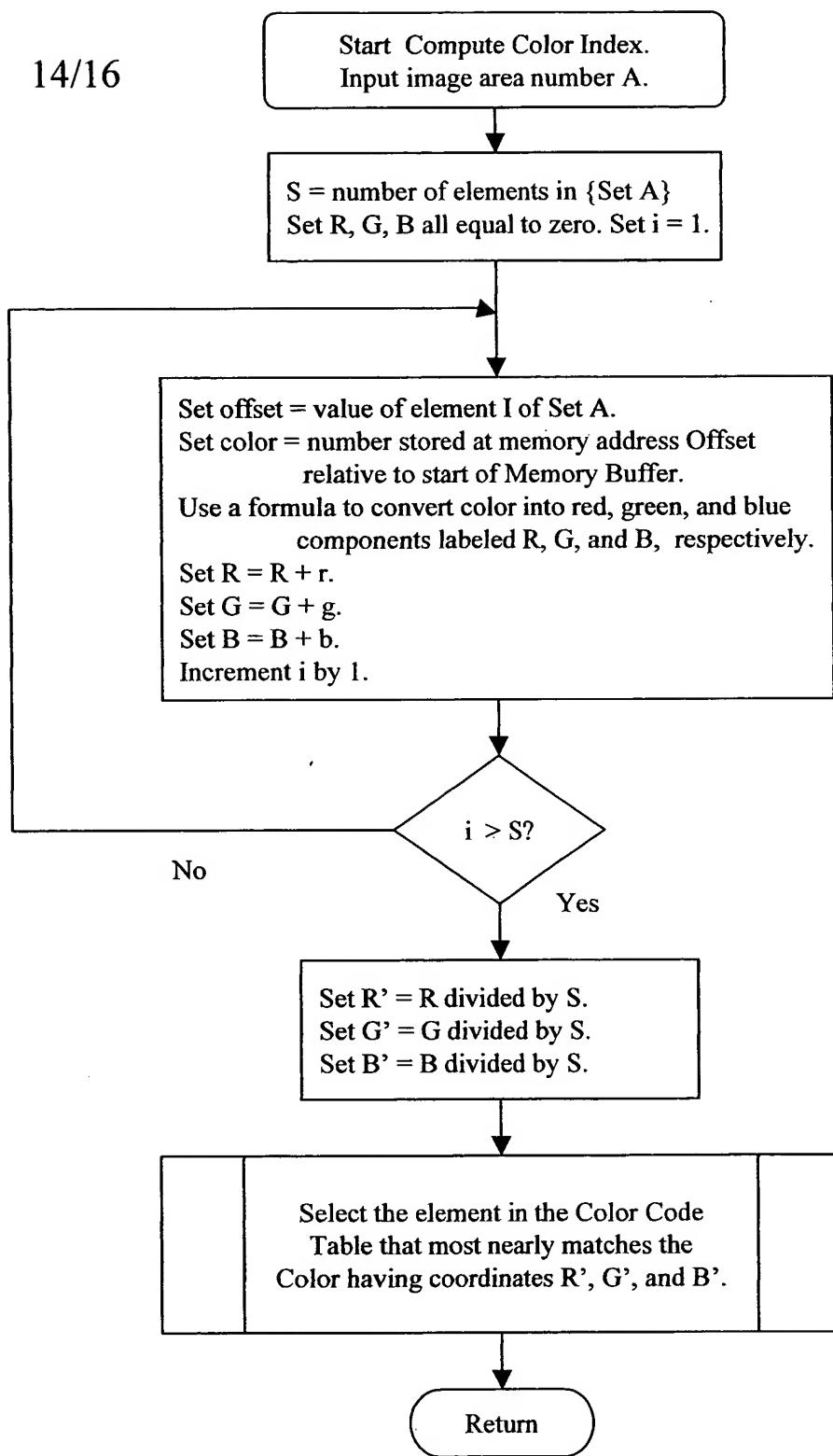


Fig. 12

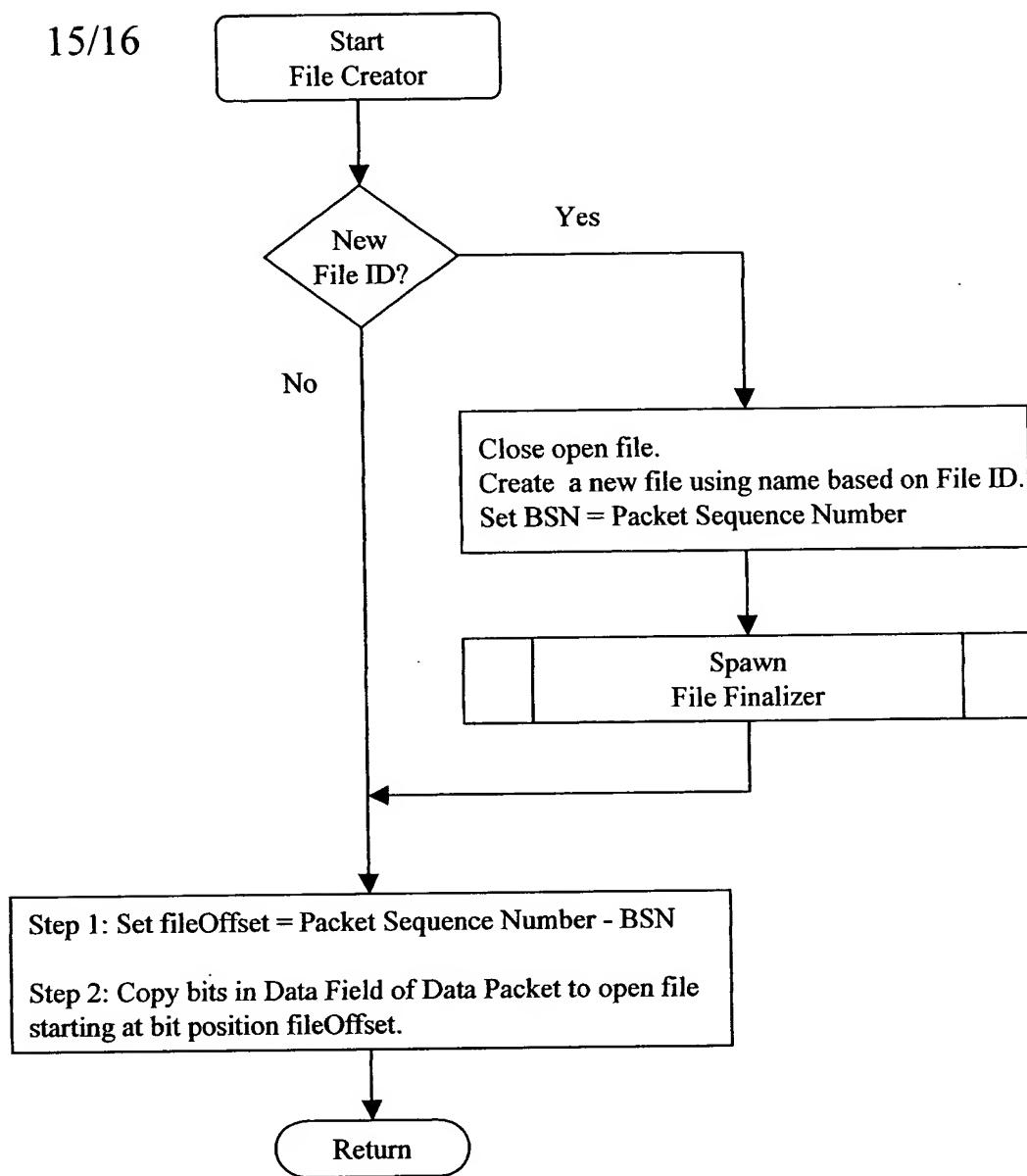


Fig. 13

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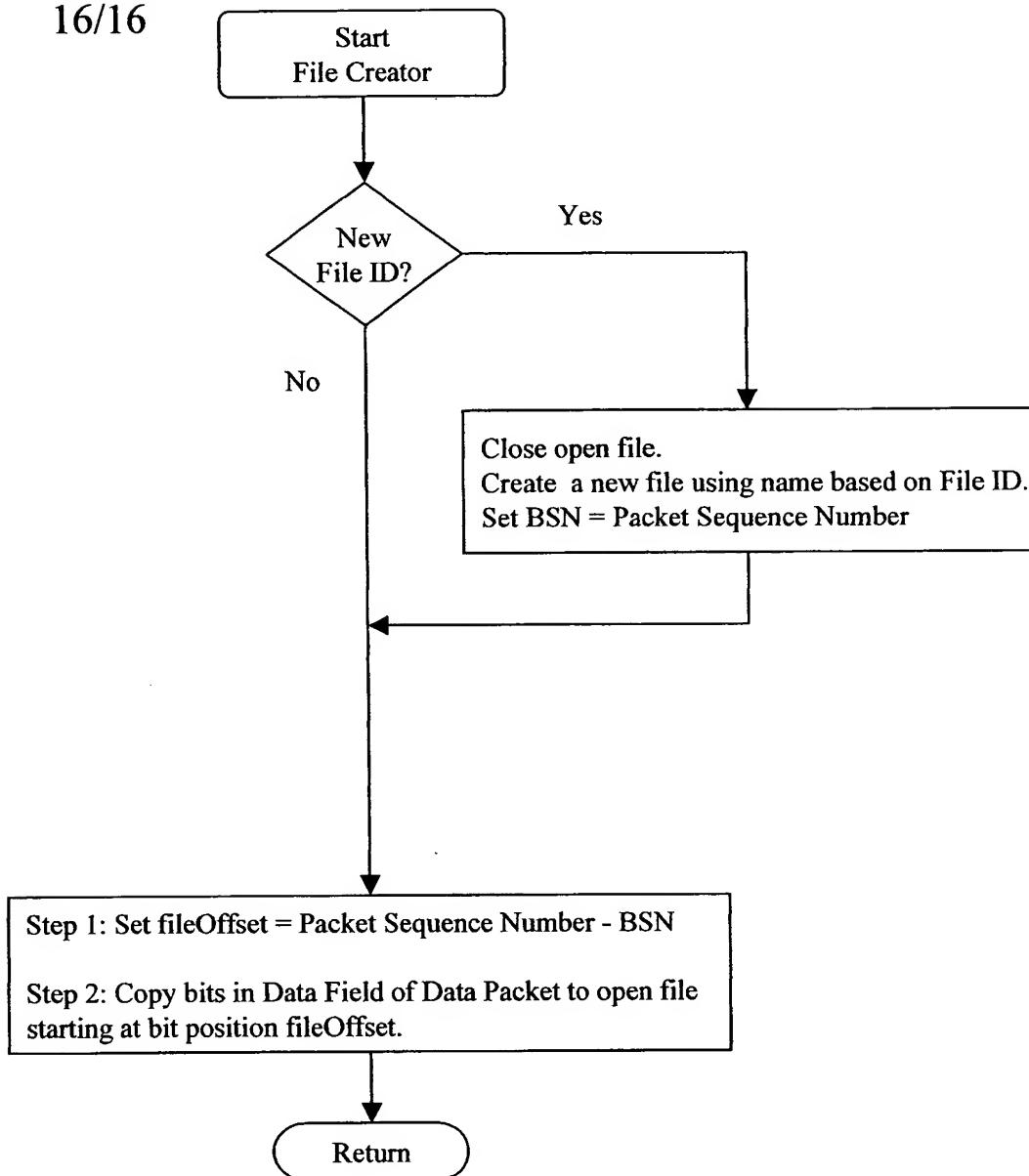


Fig. 13a